
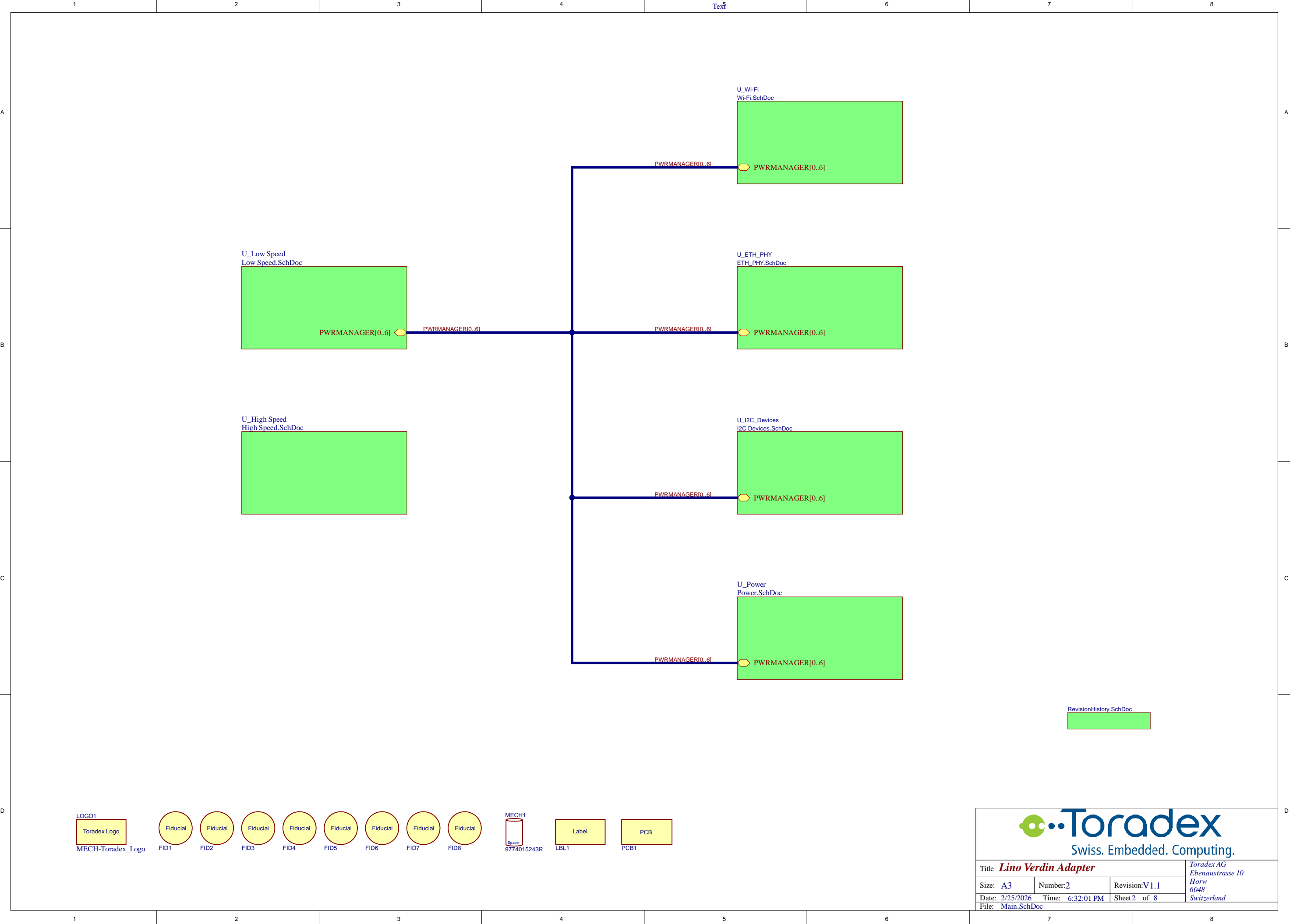



1	2	3	4	5	6	7	8
A	<div>REVISION HISTORY</div> <div>Revision V1.0</div> <div>Initial release</div> <div>Revision V1.1</div> <div>Update footprint of Lino module connector (was mirrored)</div>						A
B							B
C							C
D							D
1	2	3	4	5	6	7	8

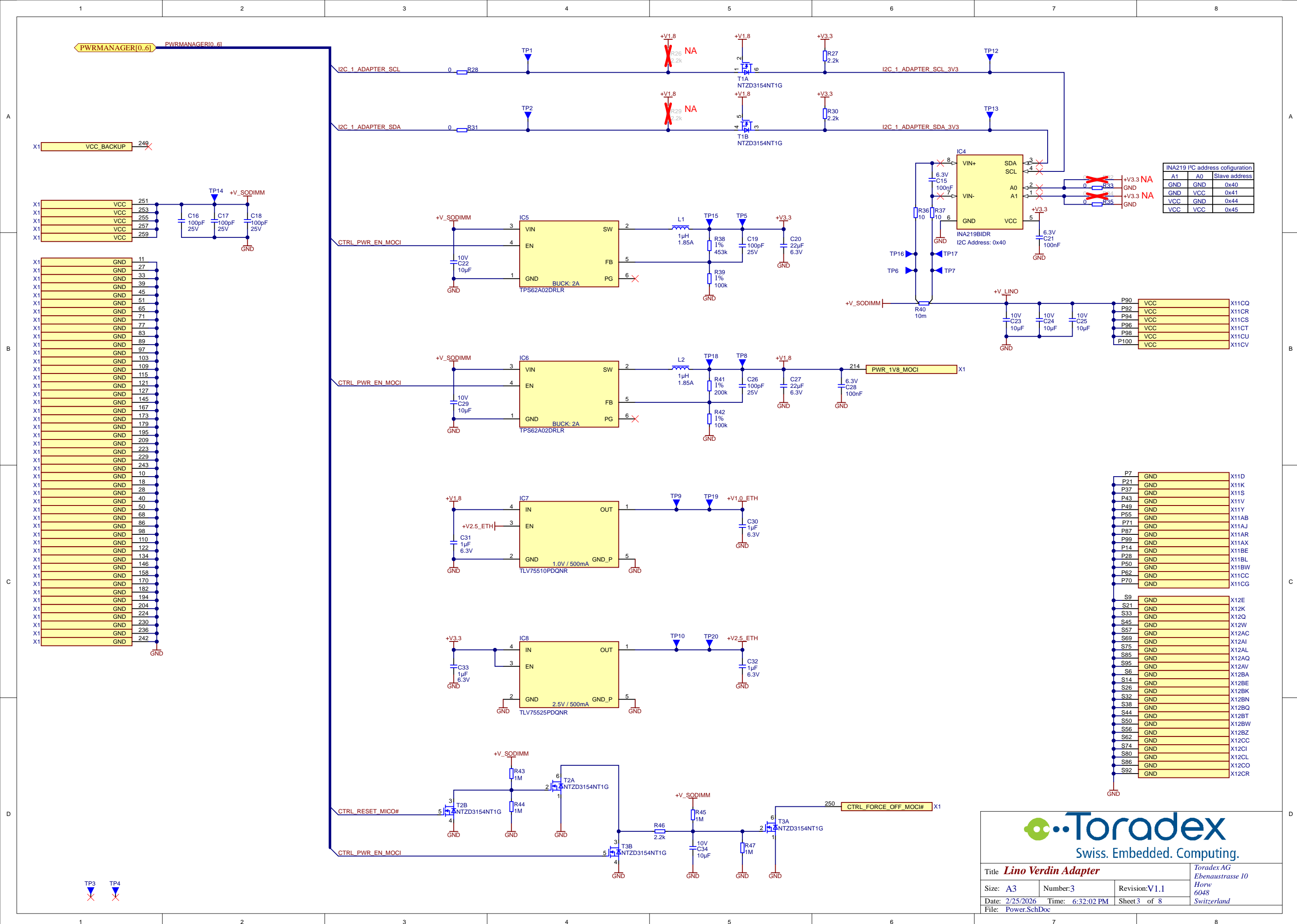


Swiss. Embedded. Computing.

Title <i>Lino Verdin Adapter</i>			Toradex AG Ebenastrasse 10 Horw 6048 Switzerland
Size: A3	Number: 1	Revision: V1.1	
Date: 2/25/2026	Time: 6:32:01 PM	Sheet 1 of 8	
File: RevisionHistory.SchDoc			



 <h1>Toradex</h1> <p>Swiss. Embedded. Computing.</p>				
Title <i>Lino Verdin Adapter</i>			<i>Toradex AG Ebenastrasse 10 Horw 6048 Switzerland</i>	
Size: A3	Number: 2	Revision: V1.1		
Date: 2/25/2026	Time: 6:32:01 PM	Sheet 2 of 8		
File: Main.SchDoc				



PWRMANAGER[0..6] PWRMANAGER[0..6]

X11A	PWM_1	P1	PWM_1	15	PWM_1	X1
X11B	PWM_2	P3	PWM_2	16	PWM_2	X1
X11C	PWM_3	P5	PWM_3	19	PWM_3_DSI	X1

X11BM	GPIO_1	P30	GPIO_1	206	GPIO_1	X1
X11BN	GPIO_2	P32	GPIO_2	208	GPIO_2	X1
X11BO	GPIO_3	P34	GPIO_3	210	GPIO_3	X1
X11BP	GPIO_4	P36	GPIO_4	212	GPIO_4	X1
X11BQ	GPIO_5	P38	GPIO_5	216	GPIO_5_CSI	X1
X11BR	GPIO_6	P40	GPIO_6	218	GPIO_6_CSI	X1
X11BS	GPIO_7	P42	GPIO_7	220	GPIO_7_CSI	X1
X11BT	GPIO_8	P44	GPIO_8	222	GPIO_8_CSI	X1
X11BU	GPIO_9	P46	GPIO_9	17	GPIO_9_DSI	X1
X11BV	GPIO_10	P48	GPIO_10	21	GPIO_10_DSI	X1

X11BH	I2C_2_SDA	P20	I2C_2_SDA	53	I2C_2_DSI_SDA	X1
X11BI	I2C_2_SCL	P22	I2C_2_SCL	55	I2C_2_DSI_SCL	X1

X11BJ	I2C_3_SDA	P24	I2C_3_SDA	93	I2C_4_CSI_SDA	X1
X11BK	I2C_3_SCL	P26	I2C_3_SCL	95	I2C_4_CSI_SCL	X1

X129	UART_1_RXD				UART_1_RXD	X1
X131	UART_1_TXD				UART_1_TXD	X1
X133	UART_1_RTS				UART_1_RTS	X1
X135	UART_1_CTS				UART_1_CTS	X1

X11G	UART_2_RXD	P13	UART_2_RXD	137	UART_2_RXD	X1
X11H	UART_2_TXD	P15	UART_2_TXD	139	UART_2_TXD	X1
X12C	UART_2_RTS	S5	UART_2_RTS	141	UART_2_RTS	X1
X12D	UART_2_CTS	S7	UART_2_CTS	143	UART_2_CTS	X1

X11I	UART_3_RXD	P17	UART_3_RXD	147	UART_3_RXD	X1
X11J	UART_3_TXD	P19	UART_3_TXD	149	UART_3_TXD	X1

X11L	UART_4_RXD	P23	UART_4_RXD	151	UART_4_RXD	X1
X11M	UART_4_TXD	P25	UART_4_TXD	153	UART_4_TXD	X1

X11N	USB_1_EN	P27	USB_1_EN	155	USB_1_EN	X1
X11O	USB_1_OC#	P29	USB_1_OC#	157	USB_1_OC#	X1
X11P	USB_1_VBUS	P31	USB_1_VBUS	159	USB_1_VBUS	X1

X11AE	USB_2_EN	P61	USB_2_EN	185	USB_2_EN	X1
X11AF	USB_2_OC#	P63	USB_2_OC#	187	USB_2_OC#	X1

X12AJ	ETH_2_RGMII_RXC	S71	ETH_2_RGMII_RXC	197	ETH_2_RGMII_RXC	X1
X12AK	ETH_2_RGMII_RX_CTL	S73	ETH_2_RGMII_RX_CTL	199	ETH_2_RGMII_RX_CTL	X1
X12AM	ETH_2_RGMII_RXD_0	S77	ETH_2_RGMII_RXD0	201	ETH_2_RGMII_RXD_0	X1
X12AN	ETH_2_RGMII_RXD_1	S79	ETH_2_RGMII_RXD1	203	ETH_2_RGMII_RXD_1	X1
X12AO	ETH_2_RGMII_RXD_2	S81	ETH_2_RGMII_RXD2	205	ETH_2_RGMII_RXD_2	X1
X12AP	ETH_2_RGMII_RXD_3	S83	ETH_2_RGMII_RXD3	207	ETH_2_RGMII_RXD_3	X1

X12AR	ETH_2_RGMII_TXC	S87	ETH_2_RGMII_TXC	213	ETH_2_RGMII_TXC	X1
X12AS	ETH_2_RGMII_TX_CTL	S89	ETH_2_RGMII_TX_CTL	211	ETH_2_RGMII_TX_CTL	X1
X12AX	ETH_2_RGMII_TXD_0	S99	ETH_2_RGMII_TXD0	221	ETH_2_RGMII_TXD_0	X1
X12AW	ETH_2_RGMII_TXD_1	S97	ETH_2_RGMII_TXD1	219	ETH_2_RGMII_TXD_1	X1
X12AU	ETH_2_RGMII_TXD_2	S93	ETH_2_RGMII_TXD2	217	ETH_2_RGMII_TXD_2	X1
X12AT	ETH_2_RGMII_TXD_3	S91	ETH_2_RGMII_TXD3	215	ETH_2_RGMII_TXD_3	X1

X11AY	ADC_1	P2	ADC_1	2	ADC_1	X1
X11AZ	ADC_2	P4	ADC_2	4	ADC_2	X1
X12A	ADC_3	S1	ADC_3	6	ADC_3	X1
X12B	ADC_4	S3	ADC_4	8	ADC_4	X1

X11BC	CAN_1_TX	P10	CAN_1_TX	20	CAN_1_TX	X1
X11BD	CAN_1_RX	P12	CAN_1_RX	22	CAN_1_RX	X1

X12BL	CAN_2_TX	S28	CAN_2_TX	24	CAN_2_TX	X1
X12BM	CAN_2_RX	S30	CAN_2_RX	26	CAN_2_RX	X1

X12CS	I2S_1_BCLK	S94	I2S_1_BCLK	30	I2S_1_BCLK	X1
X12CT	I2S_1_SYNC	S96	I2S_1_SYNC	32	I2S_1_SYNC	X1
X12CU	I2S_1_D_OUT	S98	I2S_1_D_OUT	34	I2S_1_D_OUT	X1
X12CV	I2S_1_D_IN	S100	I2S_1_D_IN	36	I2S_1_D_IN	X1

X12BH	SPI_2_CLK	S20	SPI_2_CLK	52	QSPI_1_CLK	X1
X12BG	SPI_2_CS	S18	SPI_2_CS	54	QSPI_1_CS#	X1
X12BI	SPI_2_MOSI	S22	SPI_2_MOSI	56	QSPI_1_IO0	X1
X12BJ	SPI_2_MISO	S24	SPI_2_MISO	58	QSPI_1_IO1	X1

X11CE	SD_1_CMD	P66	SD_1_CMD	74	SD_1_CMD	X1
X11CF	SD_1_CLK	P68	SD_1_CLK	78	SD_1_CLK	X1
X11CI	SD_1_D0	P74	SD_1_D0	80	SD_1_D0	X1
X11CJ	SD_1_D1	P76	SD_1_D1	82	SD_1_D1	X1
X11CB	SD_1_D2	P60	SD_1_D2	70	SD_1_D2	X1
X11CD	SD_1_D3	P64	SD_1_D3	72	SD_1_D3	X1
X11CK	SD_1_CD#	P78	SD_1_CD#	84	SD_1_CD#	X1
X11CH	SD_1_PWR_EN	P72	SD_1_PWR_EN	76	SD_1_PWR_EN	X1

X11BX	SPI_1_CLK	P52	SPI_1_CLK	196	SPI_1_CLK	X1
X11BY	SPI_1_MISO	P54	SPI_1_MISO	198	SPI_1_MISO	X1
X11BZ	SPI_1_MOSI	P56	SPI_1_MOSI	200	SPI_1_MOSI	X1
X11CA	SPI_1_CS	P58	SPI_1_CS	202	SPI_1_CS	X1

X11CM	CTRL_PWR_BTN_MICO#	P82	CTRL_PWR_BTN_MICO#	248	CTRL_PWR_BTN_MICO#	X1
X11CL	CTRL_RECOVERY_MICO#	P80	CTRL_RECOVERY_MICO#	246	CTRL_RECOVERY_MICO#	X1

X11CO	CTRL_RESET_MOCI#	P86	CTRL_RESET_MOCI#	258	CTRL_RESET_MOCI#	X1
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X11CP	CTRL_RESET_MICO#	P88	CTRL_RESET_MICO#	260	CTRL_RESET_MICO#	X1
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X11CN	CTRL_PWR_EN_MOCI	P84	CTRL_PWR_EN_MOCI	254	CTRL_PWR_EN_MOCI	X1
				256	CTRL_SLEEP_MOCI#	X1

X61	HDMI_1_HPD				HDMI_1_HPD	X1
X63	HDMI_1_CEC				HDMI_1_CEC	X1
X59	I2C_3_HDMI_SCL				I2C_3_HDMI_SCL	X1

X164	MSP_33				MSP_33	X1
X166	MSP_34				MSP_34	X1
X168	MSP_35				MSP_35	X1
X176	MSP_38				MSP_38	X1

X189	ETH_2_RGMII_INT#				ETH_2_RGMII_INT#	X1
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X21	POGO	1	JTAG_1_TDI	1	JTAG_1_TDI	X1
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CON-TE-Connectivity-1551573-5
1551573-5

X23	POGO	1	JTAG_1_TDO	5	JTAG_1_TDO	X1
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CON-TE-Connectivity-1551573-5
1551573-5

X26	POGO	1	JTAG_1_VREF	7	JTAG_1_VREF	X1
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CON-TE-Connectivity-1551573-5
1551573-5

X27	POGO	1	JTAG_1_TRST#	3	JTAG_1_TRST#	X1
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CON-TE-Connectivity-1551573-5
1551573-5

X29	POGO	1	JTAG_1_TCK	9	JTAG_1_TCK	X1
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CON-TE-Connectivity-1551573-5
1551573-5

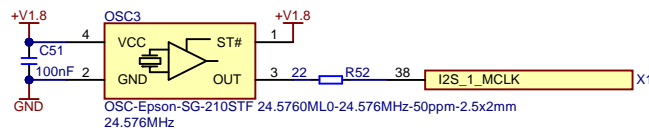
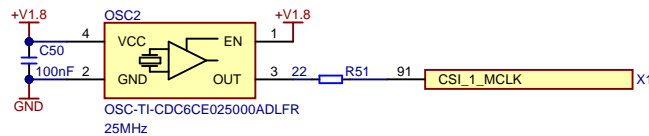
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CON-TE-Connectivity-1551573-5
1551573-5

+V1.8
R50
100k
CTRL_RESET_MOCI#
NA

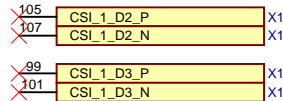
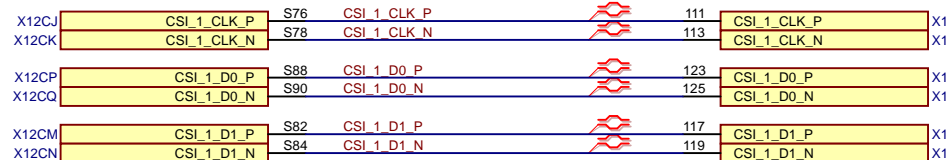
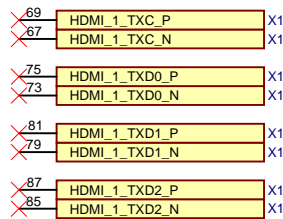
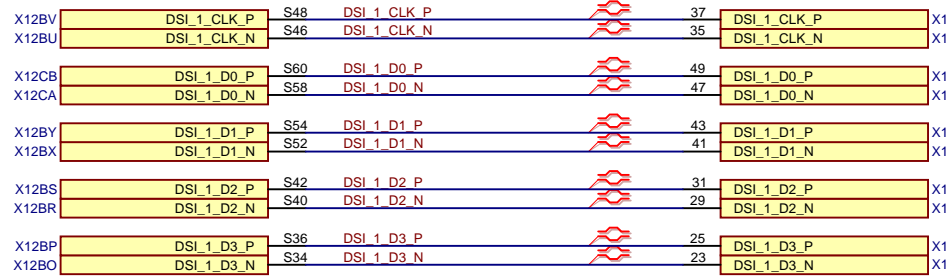
CTRL_RESET_MICO#

CTRL_PWR_EN_MOCI

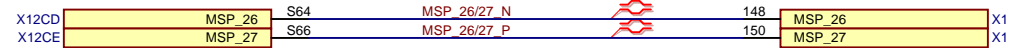
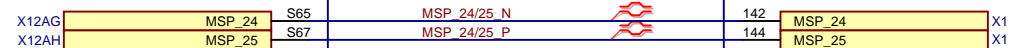
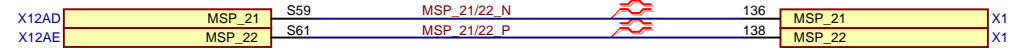
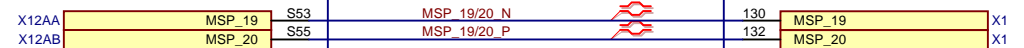
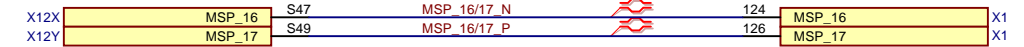
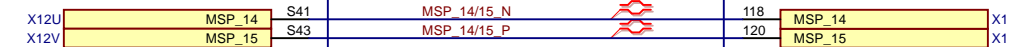
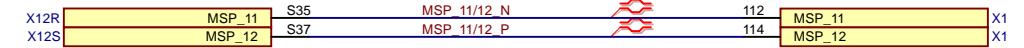
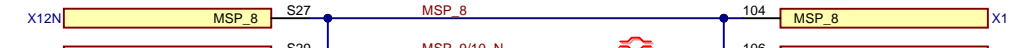
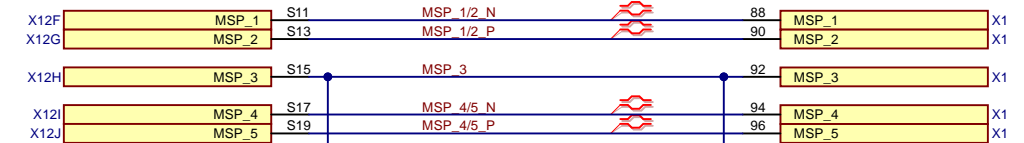
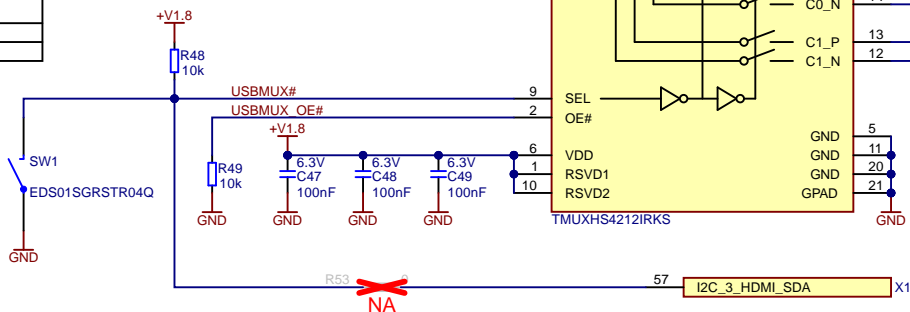


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Size:	A3	Number:	4	Revision:	V1.1
Date:	2/25/2026	Time:	6:32:02 PM	Sheet	4 of 8
File:	Low Speed.SchDoc				

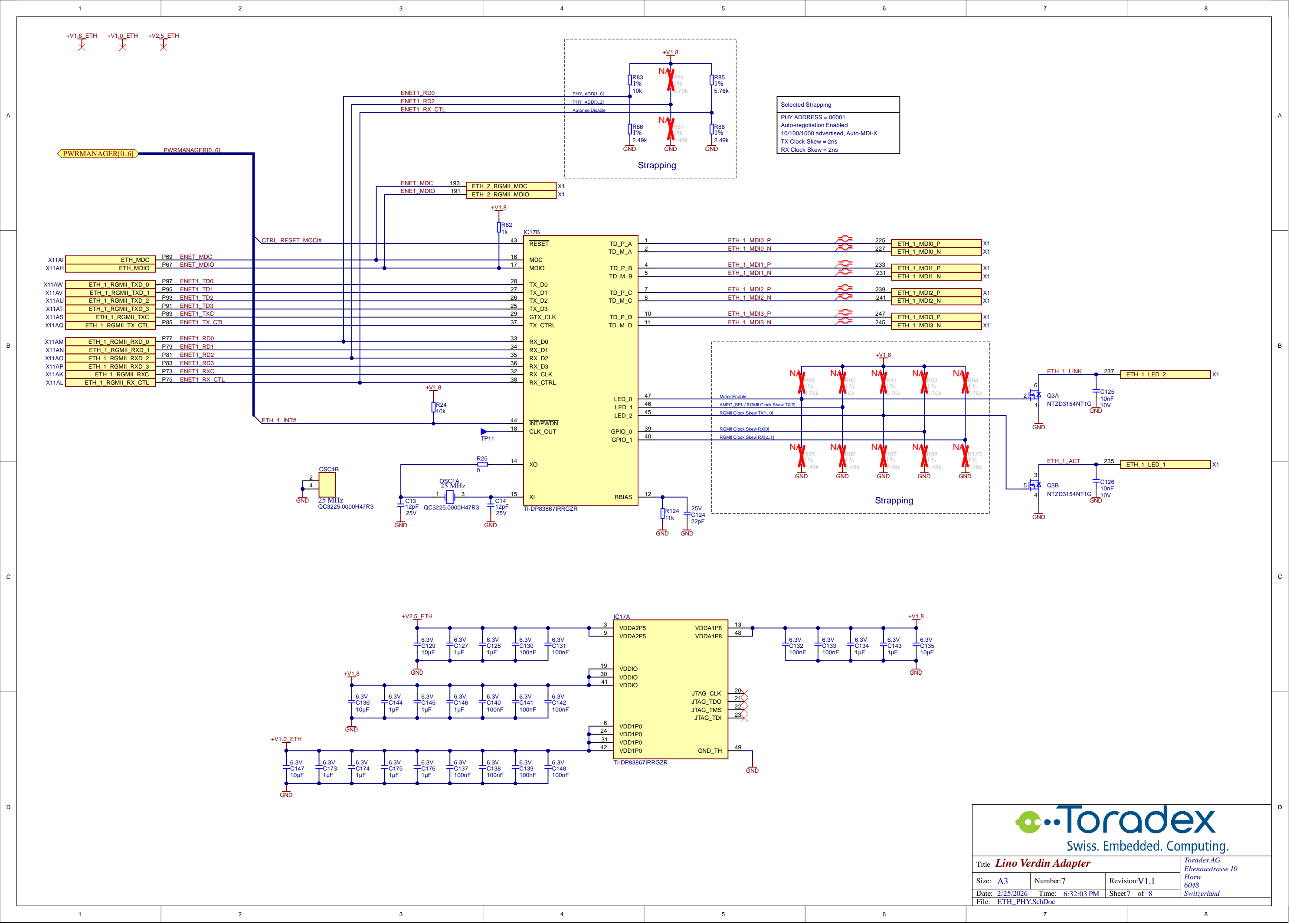
+V1.8



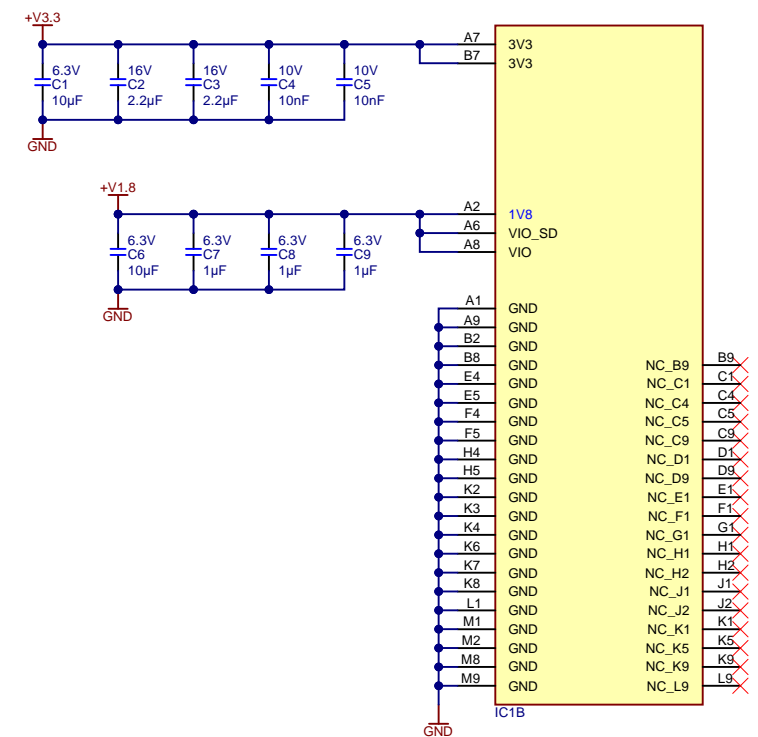
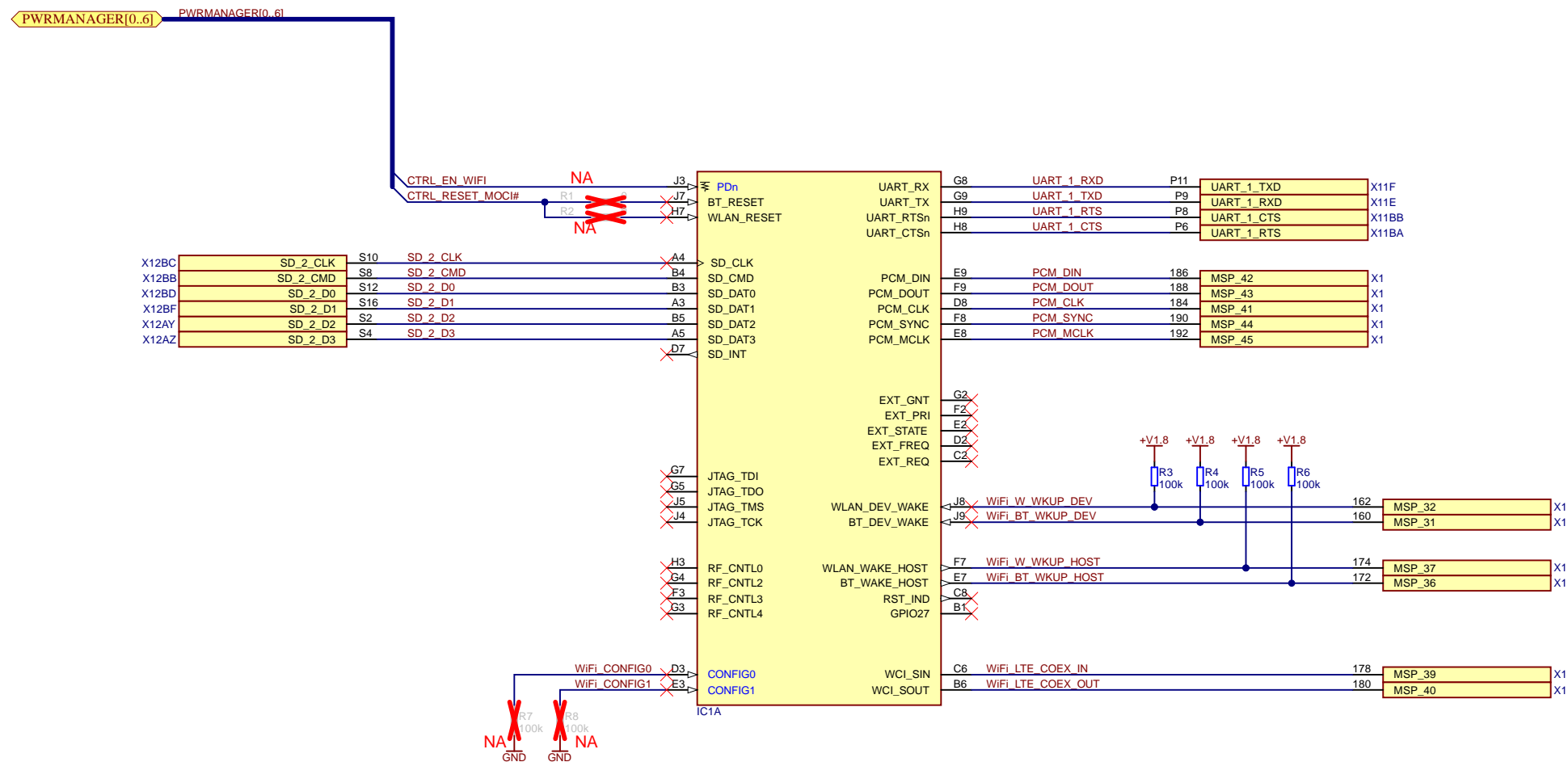
SW1	MSP Signals
Open	PCie
Closed	USB_SS



Title Lino Verdin Adapter			Toradex AG Ebenaustrasse 10 Horw 6048 Switzerland
Size: A3	Number: 5	Revision: V1.1	
Date: 2/25/2026	Time: 6:32:03 PM	Sheet 5 of 8	
File: High Speed.SchDoc			



Title <i>Lino Verdin Adapter</i>			<i>Toradex AG Ebenaustrasse 10 Horw 6048 Switzerland</i>
Size: A3	Number: 7	Revision: V1.1	
Date: 2/25/2026	Time: 6:32:03 PM	Sheet 7 of 8	
File: ETH_PHY.SchDoc			



CONFIG[1:0]	Wi-Fi	Bluetooth
00	Reserved	Reserved
01	Reserved	Reserved
10	Reserved	Reserved
11	SDIO	UART